Amendments to the Claims:

This listing of claims will replace all prior versions, and listings of claims in the application:

Listing of Claims:

1.-22. (Canceled)

23. (Currently Amended) A computer system of claim [[22]] 24,

wherein when[[.]] the disk power supply control circuit ~~instruction part~~ determines that the first logical unit is to be accessed by the disk controller ~~access request part~~, the disk power supply control circuit ~~instruction part~~ instructs the controller to turn on the first disk device configuring the first logical unit, and

wherein after the disk power supply control circuit ~~instruction part~~ instructs the controller to turn on the first disk device configuring the first logical unit, the disk controller ~~access request part~~ starts accessing to the first logical unit.

24. (Currently Amended) A computer system comprising: ~~of claim 22,~~

a storage system including a first disk device configuring a first logical unit, a second disk device configuring a second logical unit, and a controller for accessing to the first logical unit and the second logical unit; and

a disk controller requesting an access to the first logical unit and the second logical unit and a disk power supply control circuit instructing the controller to turn on or off the first disk device configuring the first logical unit and the second disk device configuring the second logical unit,

wherein after the disk power supply control circuit determines that the disk controller terminates requesting the access to the first logical unit, the disk power supply control circuit issues an instruction to turn off the first disk device configuring the first logical unit to the controller,

wherein, based on the instruction, the controller turns off the first disk device configuring the first logical unit independently of the second disk device configuring the second logical unit,

wherein the disk controller ~~access request part~~ is configured to request an access to the second logical unit after a completion of accessing to the first logical unit,

wherein the disk power supply control circuit ~~instruction part~~ determines whether or not remaining time until a scheduled time of terminating the access to the first logical unit is shorter than a predetermined time, and

wherein when the remaining time becomes shorter than a predetermined time, the disk power supply control circuit ~~instruction part~~ instructs the controller to turn on the second disk device configuring the second logical unit.

25. (Currently Amended) A computer system of claim [[22]] 24,

wherein the disk controller ~~access request part~~ is configured to request an access to the second logical unit after a completion of accessing to the first logical unit,

wherein the disk power supply control circuit ~~instruction part~~ determines whether or not the disk controller ~~access request part~~ starts accessing to the second logical unit, and

wherein when the disk controller ~~access request part~~ starts accessing to the second logical unit, the disk power supply control circuit ~~instruction part~~ instructs the controller to turn off the first disk device configuring the first logical unit.

26. (Currently Amended) A computer system of claim 25,

wherein the disk power supply control circuit ~~instruction part~~ determines whether or not the access to the second logical unit terminates, and

wherein when the access from the disk controller ~~access request part~~ to the second logical unit terminates, the disk power supply control circuit ~~instruction part~~ instructs the controller to turn off the second disk device configuring the second logical[[,]] unit.

27. (Canceled)

28. (Currently Amended) A method of claim [[27]] 29, further comprising steps of:

instructing the controller to turn on the first disk device configuring the first logical unit by the disk power supply control circuit ~~instruction part~~, when the disk power supply control circuit ~~instruction part~~ determines that the first logical unit is to be accessed by the disk controller ~~access request part~~; and

starting accessing to the first logical unit by the disk controller ~~access request part~~ after the disk power supply control circuit ~~instruction part~~ instructs the controller to turn on the first disk device configuring the first logical unit.

29. (Currently Amended) ~~A method of claim 27,~~ In a method used in a computer system which comprises:

a storage system including a first disk device configuring a first logical unit, a second disk device configuring a second logical unit, and a controller for accessing to the first logical unit and the second logical unit; and

a disk controller requesting an access to the first logical unit and the second logical unit and a disk power supply control circuit instructing the controller to turn on or off the first disk device configuring the first logical unit and the second disk device configuring the second logical unit,

the method comprising steps of:

issuing an instruction to turn off the first disk device configuring the first logical unit to the controller by the disk power supply control circuit, after the disk power supply control circuit determines that the disk controller terminates requesting the access to the first logical unit; and

turning off the first disk device configuring the first logical unit independently of the second disk device configuring the second logical unit based on the instruction by the controller;

wherein the disk controller ~~access request part~~ is configured to request an access to the second logical unit after a completion of accessing to the first logical unit, and

the method further comprising steps of:

determining by the disk power supply control circuit ~~instruction part~~ whether or not the remaining time until a scheduled time of terminating the access to the first logical unit is shorter than a predetermined time; and

instructing the controller to turn on the second disk device configuring the second logical unit, when the remaining time becomes shorter than a predetermined time.

30. (Currently Amended) A method of claim [[27]] 29, wherein the disk controller ~~access request part~~ is configured to request an access to the second logical unit after a completion of accessing to the first logical unit, and

the method further comprising steps of:

determining by the disk power supply control circuit ~~instruction part~~ whether or not the disk controller ~~access request part~~ starts accessing to the second logical unit; and

instructing the controller to turn off the first disk device configuring the first logical unit by the disk controller ~~access request part~~ when the disk controller ~~access request part~~ starts accessing to the second logical unit.

31. (Currently Amended) A method of claim 30, further comprising steps of:

determining by the disk power supply control circuit ~~instruction part~~ whether or not the access to the second logical unit terminates, and

instructing the controller to turn off the second disk device configuring the second logical unit when the access from the disk controller ~~access request part~~ to the second logical unit terminates.

32. (Canceled)

33. (Currently Amended) A computer program product of claim [[32]] 34:

wherein when the disk power supply control ~~instruction~~ means determines that the first logical unit is to be accessed by the access request means, the disk power supply control ~~instruction~~ means instructs the controller to turn on the first disk device configuring the first logical unit, and

wherein after the disk power supply control ~~instruction~~ means instructs the controller to turn on the first disk device configuring the first logical unit, the access request means starts accessing to the first logical unit.

34. (Currently Amended) A computer program product ~~of claim 32,~~ used in a computer system, wherein the computer system includes:

a storage system including a first disk device configuring a first logical unit, a second disk device configuring a second logical unit, and a controller for accessing to the first logical unit and the second logical unit; and

a disk controller accessing to the first logical unit and second logical unit, the computer program product comprising:

a recording medium;

a disk controller means, recorded on the recording medium, for requesting an access to the first logical unit and the second logical unit; and

a disk power supply control means, recorded on the recording medium, for instructing the controller to turn on or off the first disk device configuring the first logical unit and the second disk device configuring the second logical unit,

wherein after the disk power supply control means determines that the disk controller means terminates requesting the access to the fist logical unit, the disk power supply control means issues an instruction to turn off the first disk device configuring the first logical unit to the controller,

wherein, based on the instruction, the controller turns off the first disk device configuring the first logical unit independently of the second disk device configuring the second logical unit,

wherein the disk controller ~~access request~~ means is configured to request an access to the second logical unit after a completion of accessing to the first logical unit,

wherein the disk supply instruction means determines whether or not remaining time until a scheduled time of terminating the access to the first logical unit is shorter than a predetermined time, and

wherein when the remaining time becomes shorter than a predetermined time, the disk power supply control ~~instruction~~ means instructs the controller to turn on the second disk device configuring the second logical unit.

35. (Currently Amended) A computer program product of claim [[32]] 34,

wherein the disk controller ~~access request~~ means is configured to request an access to the second logical unit after a completion of accessing to the first logical unit,

wherein the disk power supply control ~~instruction~~ means determines whether or not the disk controller ~~access request~~ means starts accessing to the second logical unit, and

wherein when the disk controller ~~access request~~ means starts accessing to the second logical unit, the disk power supply control ~~instruction~~ means instructs the controller to turn off the first disk device configuring the first logical unit.

36. (Currently Amended) A computer program product of claim 35,

wherein the disk power supply control ~~instruction~~ means determines whether or not the access to the second logical unit terminates, and

wherein when the access from the disk controller ~~access request~~ means to the second logical unit terminates, the disk power supply control ~~instruction~~ means instructs the controller to turn off the second disk device configuring the second logical unit.